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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/716,195	11/17/2000	Virgil Wilkins	5127P001	6178

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EXAMINER
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HUYNH, KIM NGOC

ART UNIT	PAPER NUMBER
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2182

13

DATE MAILED: 12/16/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

## Office Action Summary

Application No.

09/716,195

Applicant(s)

WILKINS ET AL.

Examiner

Kim Huynh

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 8/8/03.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-12, 14-21 and 28-50 is/are pending in the application.
- 4a) Of the above claim(s) 3-12, 14-21 and 28-50 is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1 and 2 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. §§ 119 and 120

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a) ☐ All b) ☐ Some \* c) ☐ None of:  
1. ☐ Certified copies of the priority documents have been received.  
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).  
\* See the attached detailed Office action for a list of the certified copies not received.
- 13) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application) since a specific reference was included in the first sentence of the specification or in an Application Data Sheet. 37 CFR 1.78.  
a) ☐ The translation of the foreign language provisional application has been received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121 since a specific reference was included in the first sentence of the specification or in an Application Data Sheet. 37 CFR 1.78.

### Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892) 4) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) ☐ Notice of Informal Patent Application (PTO-152)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) 1 and 10. 6) ☐ Other: \_\_\_\_\_

## **DETAILED ACTION**

### ***Election/Restrictions***

1. Claims 3-12, 14-21 and 28-50 are withdrawn from further consideration pursuant to 37 CFR 1.142(b) as being drawn to a nonelected species, there being no allowable generic or linking claim. Election was made **without** traverse in Paper No. 9.

Please note that applicant indicates the elections of species 4, Fig. 6, claims 1, 6, 36, 39, 40-41, 43 and 47. However, claims 6, 36, 39, 40, 41, 43, and 47 cover various different species and do not seem to properly directed to the disk mapping controller of species 4, Fig. 6 as indicated.

A telephone interview with Mr. Stephen A. Soffen on December 8, 2003, the examiner indicated that the election of Species 1, claims 1-2 indicated in paper number 9 was overlooked and the current election of claims 1, 6, 36, 39, 40-41, 43 and 47 is not proper as discussed above. As a result, it was agreed that claims 1-2, directed to I/O controller with mapping controller originally elected in paper number 9, would be considered.

### ***Claim Rejections - 35 USC § 102***

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

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(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

3. Claims 1-2 are rejected under 35 U.S.C. 102(e) as being anticipated by Asnaashari (US 6,076,137).

Asnaashari discloses an integrated I/O controller integrated into a single IC device (col. 5, ll. 24-26) having a host interface subsystem 14, a controller device 20 for mapping the logical block address (LBA) into the block addresses (PBA) of one or more peripheral devices 0-M (col. 2, ll. 38-44 and col. 7, ll. 14-17) and a peripheral interface device 24 coupled to the peripheral devices.

***Claim Rejections - 35 USC § 103***

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claims 1-2 are rejected under 35 U.S.C. 103(a) as being unpatentable over Day et al. (US 6,065,096) in view of Bolt (US 6,467,014).

Day discloses an integrated I/O controller 100 integrated into a single IC device having a host interface subsystem 124, a controller device 121 for mapping the LBA into the block addresses of one or more peripheral devices 108 and a peripheral interface device 130 coupled to the peripheral devices. Day discloses the function of the

controller 100 is to map requests from hosts to the appropriate disk drives, however, Day does not explicitly disclose the mapping of the logical block address (LBA) into the block addresses of one or more peripheral devices.

Bolt discloses that it is conventional that each LBA referred to by the host computer must be mapped to the block address (STSA) of the peripheral devices in order to accommodate easy identification and retrieval (col. 1, ll. 11-27, 63-66 and col. 2, ll. 42-65). It would have been obvious to one having ordinary skill in the art to implement the teaching of mapping LBA into the block addresses (PBA) of one or more peripheral devices in order to accommodate easy identification and retrieval as taught by Bolt.

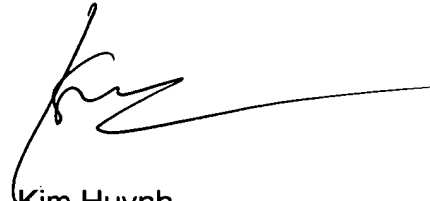
### ***Conclusion***

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Assar et al. (US 5,485,595) discloses mapping technique for mapping LBA into the block addresses (PBA) of one or more flash memory.

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kim Huynh whose telephone number is (703) 308-1678.

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Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3900.

A handwritten signature in black ink, appearing to read 'Kim Huynh', with a long horizontal flourish extending to the right.

Kim Huynh  
Primary Examiner  
Art Unit 2182

KH  
December 9, 2003